FIG.1

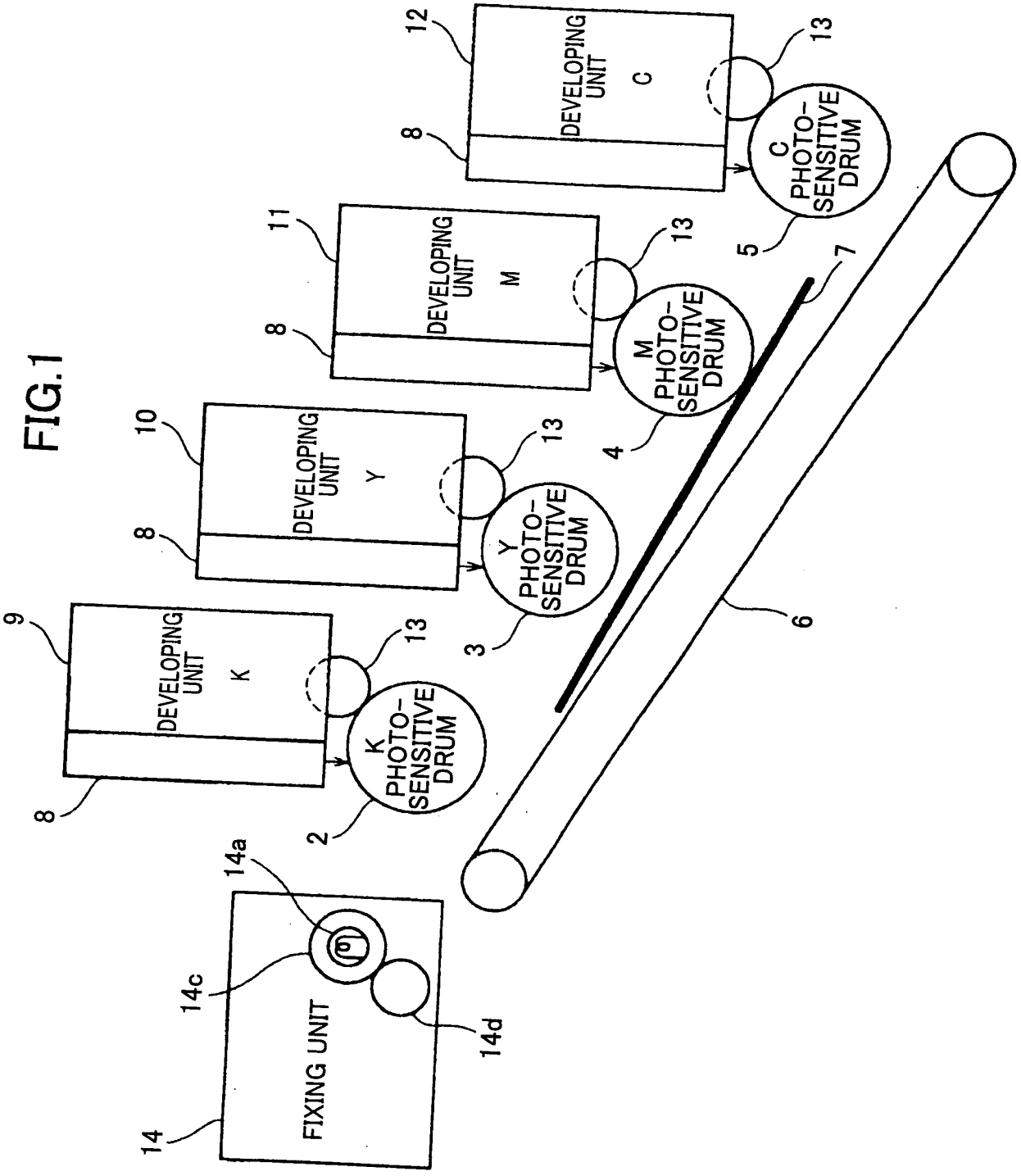

DEVELOPING UNIT K
DEVELOPING UNIT Y
DEVELOPING UNIT M
DEVELOPING UNIT C
K PHOTO-SENSITIVE DRUM
Y PHOTO-SENSITIVE DRUM
M PHOTO-SENSITIVE DRUM
C PHOTO-SENSITIVE DRUM
FIXING UNIT
2
3
4
5
6
7
8
9
10
11
12
13
14
14a
14c
14d

FIG.2

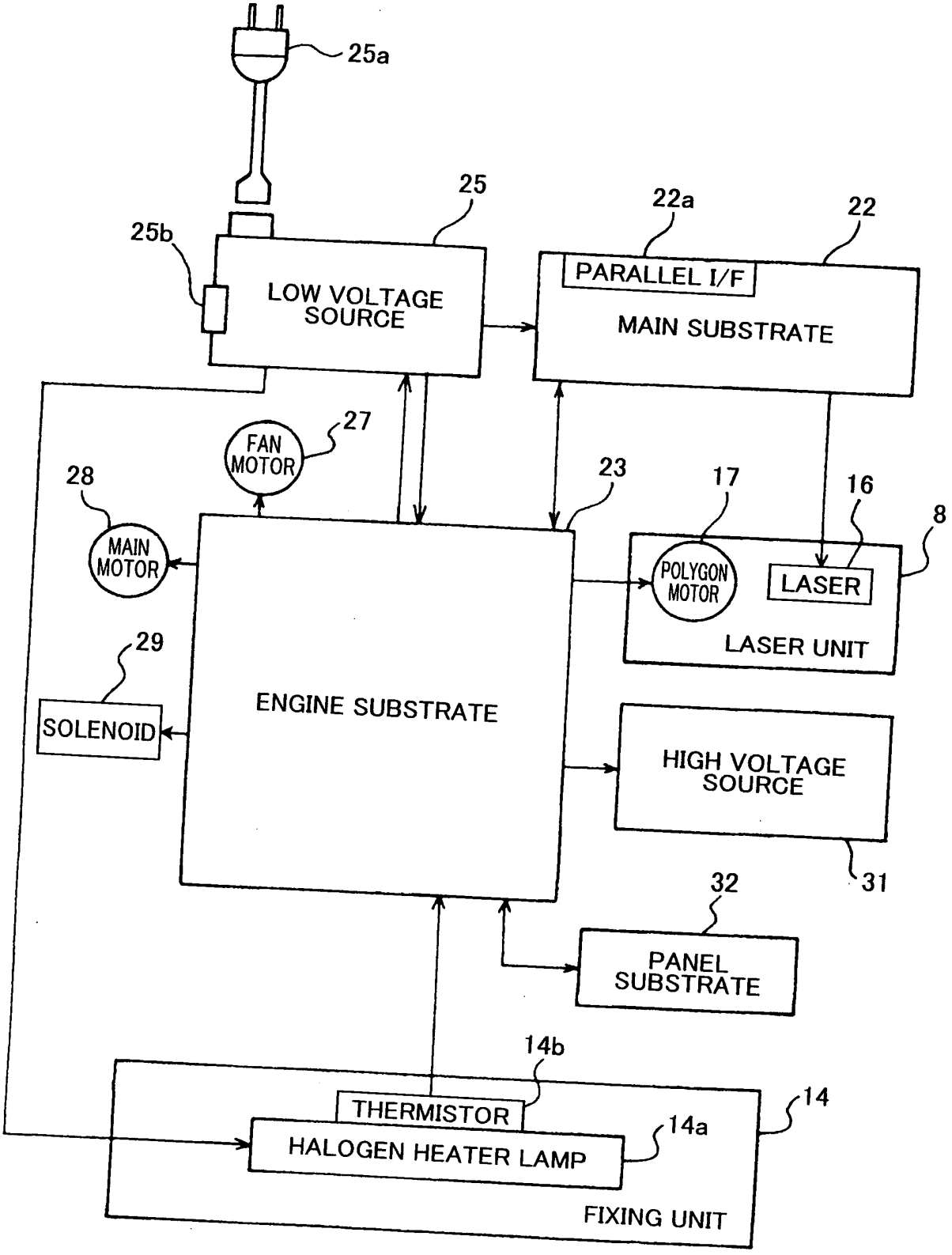

25a
25
25b
LOW VOLTAGE SOURCE
22a
22
PARALLEL I/F
MAIN SUBSTRATE
27
FAN MOTOR
28
MAIN MOTOR
23
17
16
8
POLYGON MOTOR
LASER
LASER UNIT
29
SOLENOID
ENGINE SUBSTRATE
HIGH VOLTAGE SOURCE
31
32
PANEL SUBSTRATE
14b
THERMISTOR
14
14a
HALOGEN HEATER LAMP
FIXING UNIT

FIG.3

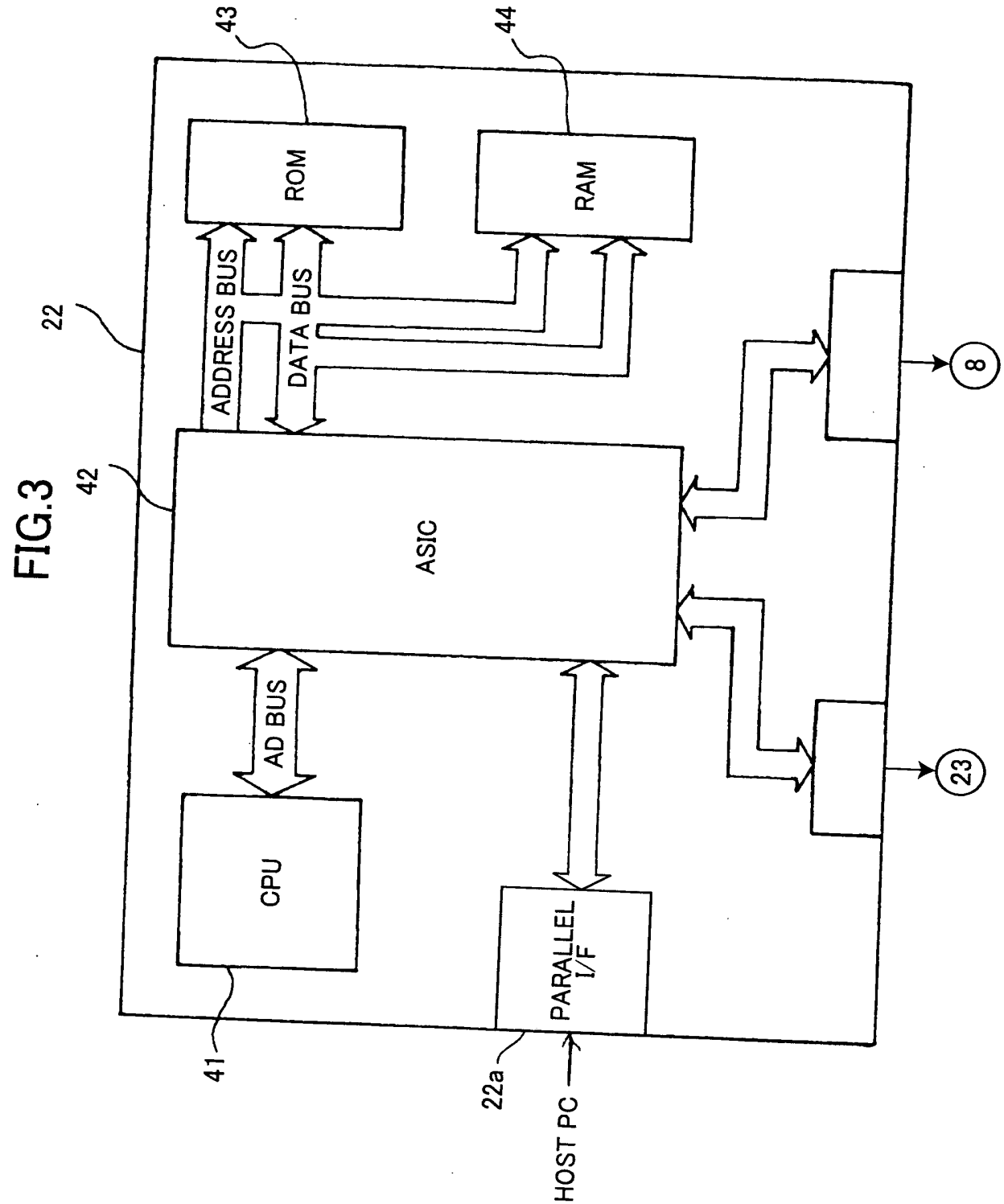

43
44
ROM
RAM
ADDRESS BUS
DATA BUS
22
42
ASIC
8
AD BUS
23
CPU
PARALLEL I/F
41
22a
HOST PC

FIG.4

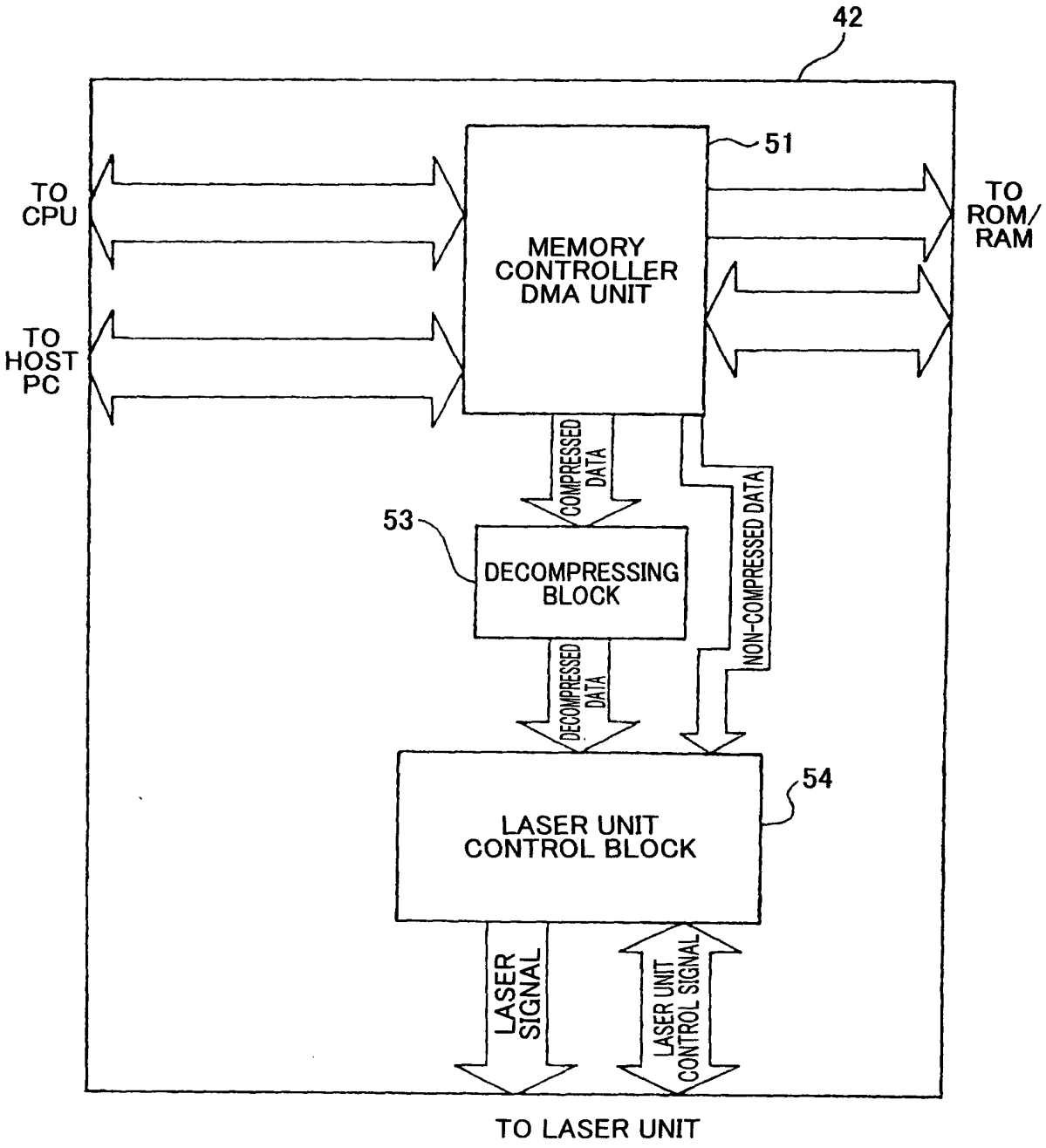

42
51
TO CPU
TO HOST PC
MEMORY CONTROLLER DMA UNIT
TO ROM/ RAM
COMPRESSED DATA
NON-COMPRESSED DATA
53
DECOMPRESSING BLOCK
DECOMPRESSED DATA
54
LASER UNIT CONTROL BLOCK
LASER SIGNAL
LASER UNIT CONTROL SIGNAL
TO LASER UNIT

FIG.5

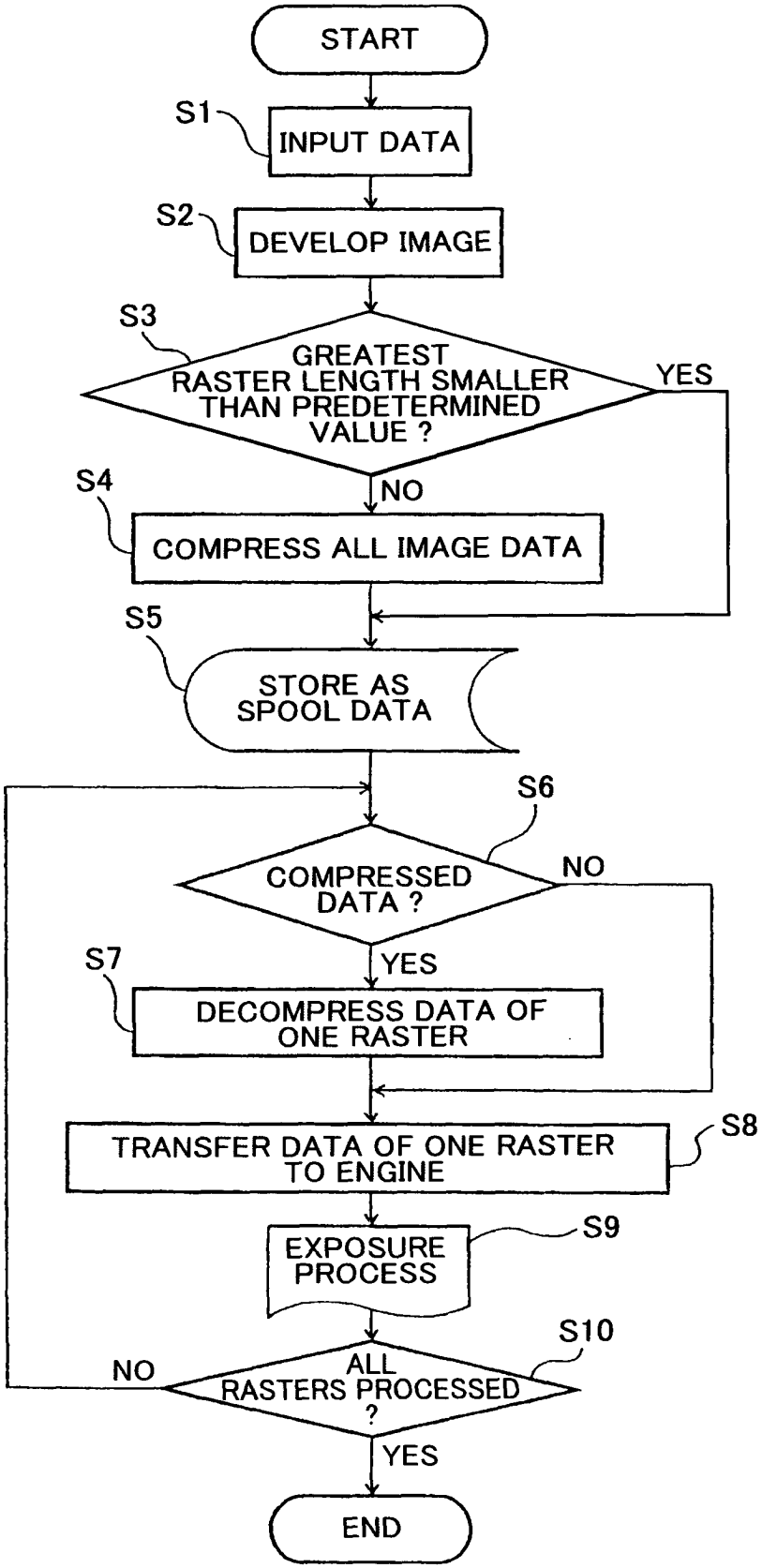
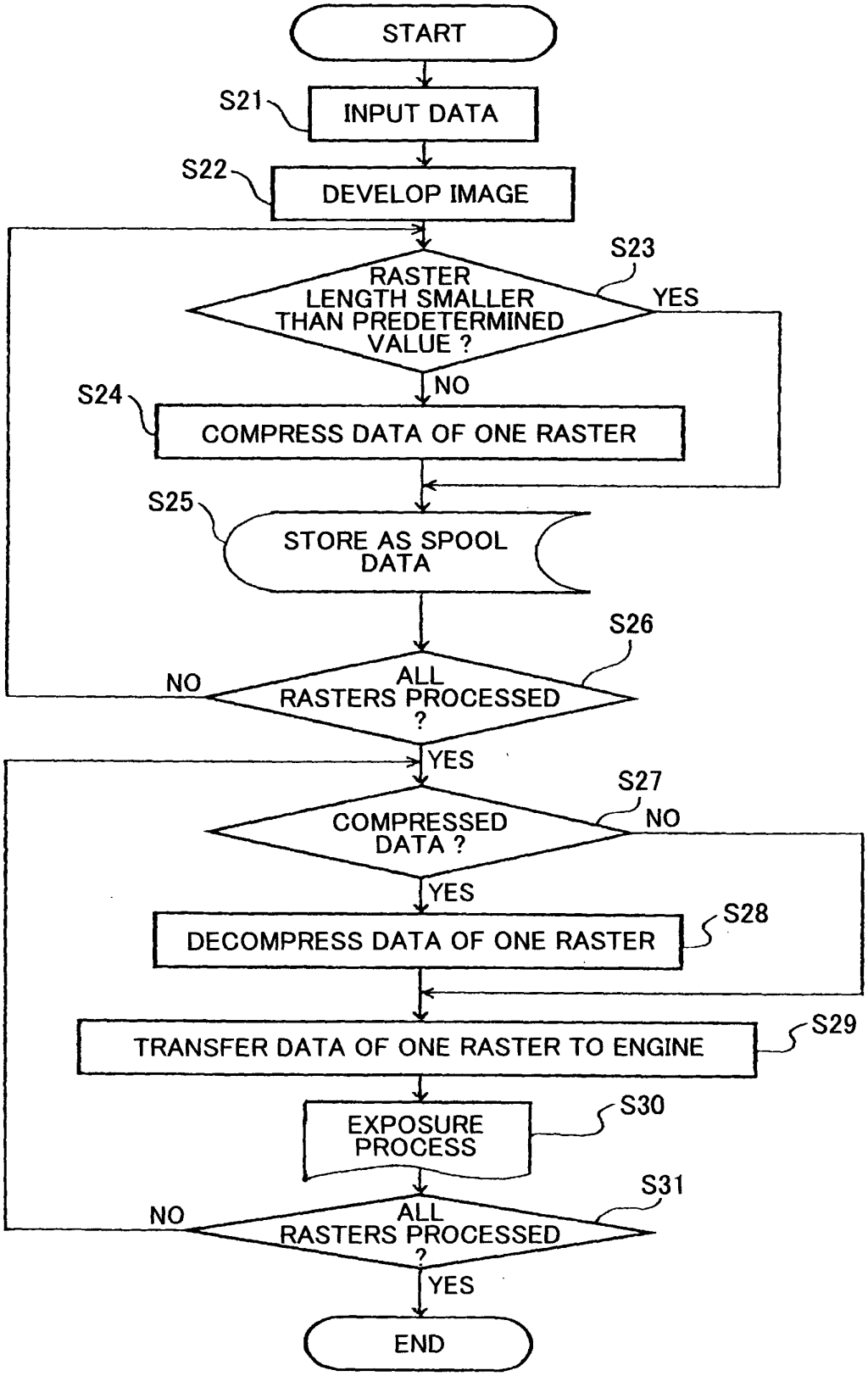

START
S1
INPUT DATA
S2
DEVELOP IMAGE
S3
GREATEST RASTER LENGTH SMALLER THAN PREDETERMINED VALUE ?
YES
NO
S4
COMPRESS ALL IMAGE DATA
S5
STORE AS SPOOL DATA
S6
COMPRESSED DATA ?
NO
YES
S7
DECOMPRESS DATA OF ONE RASTER
TRANSFER DATA OF ONE RASTER TO ENGINE
S8
EXPOSURE PROCESS
S9
S10
ALL RASTERS PROCESSED ?
NO
YES
END

FIG.6


START
S21
INPUT DATA
S22
DEVELOP IMAGE
S23
RASTER LENGTH SMALLER THAN PREDETERMINED VALUE ?
YES
NO
S24
COMPRESS DATA OF ONE RASTER
S25
STORE AS SPOOL DATA
S26
ALL RASTERS PROCESSED ?
NO
YES
S27
COMPRESSED DATA ?
NO
YES
S28
DECOMPRESS DATA OF ONE RASTER
S29
TRANSFER DATA OF ONE RASTER TO ENGINE
S30
EXPOSURE PROCESS
S31
ALL RASTERS PROCESSED ?
NO
YES
END